SPI Storm Studio – Release notes

Version 1.1.29
  • Fixed bug affecting data readback for long transfers.

Version 1.1.28
  • Corrected bug affecting non-byte-aligned transfers.

Version 1.1.27
  • Corrected bug preventing from properly finishing to read back data for specific total data lengths.

Version 1.1.26
  • Added signed drivers
  • Bug corrections

Version 1.1.25
  • Fixed problem with large readback data in SPI commands.

Version 1.1.24
  • Fixed clock issue when using external clock at very low frequencies.

Version 1.1.23
  • Fixed WE behaviour.
  • Added custom segments rules allowing control of SS2 and SS3 as constants.
  • Updated user's guide.

Version 1.1.22
  • Fixed bit order in custom quad and dual spi macros.
  • Added SPI-3 with read length = 0.

Version 1.1.21
  • Fixed data read back issue for long transfers.

Version 1.1.20
  • Fixed data corruption observed with long transfers, related to bit/byte ordering feature.
  • Fixed wizard issues.

Version 1.1.19
  • Corrected issue related to software hanging in some cases of device programming when using external voltage for I/Os.
Version 1.1.18
• Improved stability
• Added full USB 3.0 PC port support
• Support for new SPI Storm devices

Version 1.1.17: not released

Version 1.1.16
• Small fixes

Version 1.1.15
Features
• Small fix of compilation issue.

Version 1.1.14
Features
• Added option for selecting bit ordering: MSBit first. Turned default bit ordering to MSBit first.
• Improved software stability.

Version 1.1.13
Features
• Removed spurious special characters from output file.

Version 1.1.12
Features
• Resolved bug affecting long loops and programs

Version 1.1.11
Features
• Resolved potential device conflict with LOG Storm device
• Added trigger support for custom macros
• Corrected issue affecting clock frequency change within or between macros
• Added version function in DLL

Version 1.1.10
Features
• Fixed issue affecting the execution of 2 successive macros using the API without GUI
• Added check for missing GPO data – caused invalid syntax error when reading config file
• Added clock frequency field
• Increased decoding speed of large loops
Version 1.1.9
Features
• Updated GPO support from C API

Known limitations:
• Currently limited to 10 MB per run
• SPI access minimum length: 2 bits
• Minimum length for segment with data read back: 2 bits.

Version 1.1.8
Features
• Added external clock detection and support
• Added external voltage detection for notification
• Improved USB connectivity

Known limitations:
• Currently limited to 10 MB per run
• SPI access minimum length: 2 bits
• Minimum length for segment with data read back: 2 bits.

Version 1.1.7
Features
• Several bug fixes.

Features under development:
• External clock detection
• External voltage detection

Known limitations:
• Currently limited to 10 MB per run
• SPI access minimum length: 2 bits
• Minimum length for segment with data read back: 2 bits.

Version 1.1.6
Features
• Corrected issue preventing from aborting run under some configurations.
• Corrected issue affecting synchronisation between the serial port and the GPO port.
• Corrected display issue with progress bar.
• Several bug fixes.

Features under development:
• External clock detection
• External voltage detection

Revision 1.23 - 20-Jul-2015
Byte Paradigm – info@byteparadigm.com
Known limitations:
- Currently limited to 10 MB per run
- SPI access minimum length: 2 bits
- Minimum length for segment with data read back: 2 bits.

Version 1.1.5
Features
- Corrected issue preventing from directing data to user interface when DLL was used.

Features under development:
- External clock detection
- External voltage detection

Known limitations:
- Currently limited to 10 MB per run
- SPI access minimum length: 2 bits
- Minimum length for segment with data read back: 2 bits.

Version 1.1.4
Features
- Corrected issue preventing from reading back data in SPI 4 mode

Features under development:
- External clock detection
- External voltage detection

Known limitations:
- Currently limited to 10 MB per run
- SPI access minimum length: 2 bits
- Minimum length for segment with data read back: 2 bits.

Version 1.1.3
Features
- Corrected issue affecting project load with SPI Storm Studio in demo mode (no license installed)

Features under development:
- External clock detection
- External voltage detection

Known limitation:
- Currently limited to 10 MB per run
Version 1.1.2
Features
• Corrected syntax check on SPI3 protocol

Features under development:
• External clock detection
• External voltage detection

Known limitation:
• Currently limited to 10 MB per run

Version 1.1.x
Features
• Enabled wizard for standard SPI protocol
• Improved detection of device disconnect
• Improved configuration validity check
• Improved GUI stability in Windows XP
• Added open drain controls for GPO
• Added dummy cycles in Dual- and Quad-SPI protocols
• Minor bugs correction

Features under development:
• External clock detection
• External voltage detection

Known limitation:
• Currently limited to 10 MB per run

Version 1.0.0
Features
• Signed drivers now available (32-bit / 64-bit)
• Minor bugs correction
• License check enabled

Features under development:
• Multi-device support
• External clock detection
• External voltage detection

Known limitation:
• Currently limited to 10 MB per run