

## 8PI Control Panel – List of Known Issues

<b>8PI Control Panel version 1.08*</b>			
<b>Issue nr</b>	<b>Description</b>	<b>Priority</b>	<b>Actions</b>
1.	GP-24100 device / Analyser mode of operation: trigger positioning is not available	<b>Warm</b>	Will be made available in future software release
2.	GP-24100 device / ADWG mode of operation: trigger rearm is not available	<b>(Solved)</b>	Enabled from 8PI Control Panel version <b>1.08a</b>
3.	GP-24100 device / SPI Master mode of operation: SCLK frequency is limited to 50 MHz	<b>Warm</b>	<b>Update:</b> new advanced mode/software will cover this case for GP-24100.
4.	GP-24100 device: using a control sequence with an odd number of samples: the last data does not go out.	<b>(Solved)</b>	Enabled from 8PI Control Panel version <b>1.08a</b>
5.	GP-24100 device – ADWG mode – infinite loop – loop is incorrectly executed if less than 16 bits are enabled. Current work around: enable all 16 bits in infinite loop	<b>(Solved)</b>	Corrected in 8PI Control Panel <b>1.08f</b>
6.	Rev 02 /03 devices – clkout did not come out	<b>(Solved)</b>	Corrected in 8PI control Panel release <b>1.08e</b>
7	SPI master + SS shift problem : data is incorrectly aligned with clock	<b>(Solved)</b>	Corrected in 8PI control Panel release <b>1.08f</b>
8	GP-24100 – ADWG mode: made clock divider more robust	<b>(Solved)</b>	Corrected in 8PI control Panel release <b>1.08g</b>
9	GP-24100 – logic analyzer mode : software sometimes freezes in case of overflow (going over the device max bandwidth on USB). Unconnecting the device gives control again.	<b>(Solved)</b>	To be corrected in a future release.
10	GP-24100 constant control in ADWG mode is not enabled	<b>(Solved)</b>	Available from 8PI Control Panel release <b>1.08h</b>
11	SPI accesses show clock / data interruption if run length over 4.096 bits	<b>(Solved)</b>	Corrected from 8PI Control Panel version <b>1.08j</b>